

A4000DT-060 Accelerator
(date unknown)

Jumpers & I/O Definitions														
<u>Jumpers</u>	<u>Definitions</u>	<u>Default</u>												
JP1	CPU Select OFF = 68040/060 ON = Motherboard CPU	OFF												
JP2	Reserved	OFF												
JP4	Cache Burst to A4000 Motherboard OFF = Cache Burst Disabled ON = Cache Burst Enabled	OFF												
JP5	Interrupt Pending, DMA Backoffs ON = DMA Backoffs for Interrupt OFF = DMA Ignores Interrupt	OFF												
JP6	Active SCSI Termination ON = SCSI Termination Disabled OFF = SCSI Termination Enabled	OFF												
JP7	CPU Clock Disable (Test Only) ON = Clock Disabled OFF = Clock Enabled	OFF												
JP8	CPU Clock 1 and 2 ... 68040 2 and 3 ... 68060													
JP9	CPU Power 1 and 2 ... 5V ... 68040 2 and 3 ... 3.3V ... 68060 EPROM Type 27C010													
CN6	5V FAN													
CN8	SCSI LED Indicator													
JR1	Memory Configured for Burst Mode ON = Burst Mode Support (2 SIMMs Minimum Required) OFF = Non-Burst Mode (Support for any number of SIMMs)	ON												
JR2	DRAM Speed vs. CPU Clock <table style="margin-left: auto; margin-right: auto; border: none;"> <tr> <td></td> <td style="text-align: center;"><u>50MHz</u></td> <td style="text-align: center;"><u>40MHz</u></td> <td style="text-align: center;"><u>33MHz</u></td> </tr> <tr> <td>OFF =</td> <td style="text-align: center;">60ns</td> <td style="text-align: center;">60/70ns</td> <td style="text-align: center;">60/70/80ns</td> </tr> <tr> <td>ON =</td> <td style="text-align: center;">70ns</td> <td style="text-align: center;">80ns</td> <td></td> </tr> </table>		<u>50MHz</u>	<u>40MHz</u>	<u>33MHz</u>	OFF =	60ns	60/70ns	60/70/80ns	ON =	70ns	80ns		OFF
	<u>50MHz</u>	<u>40MHz</u>	<u>33MHz</u>											
OFF =	60ns	60/70ns	60/70/80ns											
ON =	70ns	80ns												

Jumpers & I/O Definitions (cont.)

<u>Jumpers</u>	<u>Definitions</u>	<u>Default</u>
JR3	Burst Write Enabled ON = Write Enabled	ON
JR4	Burst Write Enabled ON = Read Enabled	ON
JR5	Memory Size OFF = 4MB ON = 16MB	OFF
JR6	Single/Double-Sided SIMM OFF = Single-Sided SIMM ON = Double-Sided SIMM	OFF
JR7	Reserved	OFF
JR8	Refresh Mode OFF/ON ... 4K Refresh (Asymmetrical) OFF ... 2K Refresh (Symmetrical)	OFF

Note: Both symmetrical and asymmetrical DRAMs are supported when JR8 is off.

Memory Location and Size

CN1-CN4 are sockets for Industry Standard SIMMs. All four sockets can support either 4MB or 16MB single-sided SIMMs, or 8MB or 32MB double-sided SIMMs depending on the jumper settings of JR5 & JR6 as shown below.

JR5 OFF, JR6 OFF

CN1	8000000-83FFFFFF HEX
CN2	8400000-87FFFFFF HEX
CN3	8800000-8BFFFFFF HEX
CN4	8C00000-8FFFFFFF HEX

JR5 ON, JR6 OFF

CN1	8000000-8FFFFFFF HEX
CN2	9000000-9FFFFFFF HEX
CN3	A000000-AFFFFFFF HEX
CN4	B000000-BFFFFFFF HEX

JR5 OFF, JR6 ON

CN1	8000000-8FFFFFFF HEX
CN2	8800000-8FFFFFFF HEX
CN3	9000000-97FFFFFF HEX
CN4	9800000-9FFFFFFF HEX

JR5 ON, JR6 ON

CN1	8000000-9FFFFFFF HEX
CN2	A000000-BFFFFFFF HEX
CN3	C000000-DFFFFFFF HEX
CN4	E000000-FFFFFFFF HEX

*** Note: Burst will only function properly when there are even multiples of SIMMs installed.**